

Here's a few corrections to the TV Typewriter supplement:

1. Diode D6 is backwards on the power supply overlay, and the negative supply diodes are shown backwards on the schematic.
2. On figure 3 schematic, NCLR pin 25 should also go to keyboard input B and the diodes D10-14. The connection between diodes D10-14 and "C" should be deleted. The PC board is correct.
3. Callouts are missing on the keyboard edge connector. "A" is nearest the RF twinlead; "L" is nearest J1.
4. Delete R11 and R12 from the mainframe figure 3 schematic. Add R5, R6 to the mainframe parts list, 1K, 1/4 watt carbon.
5. There are several printing problems on the supplement overlays. The overlays on the kit PC boards are correct and complete.
6. On the improved ASCII encoder schematic, IC2 should be 7402. TP tie points go to pins 4 and 5 of IC4.
7. Table V. For normal use, the switch should be left in the FULL position.
8. Timing E, cursor TPH should be 10 milliseconds, not microseconds.
9. An additional 0.05 ufd disc capacitor with minimum lead lengths might be needed across the TOP of cursor IC1 (7408) from pin 7 to 14. Counter IC substitutions in the cursor might require slight shifts in pulse widths and positions. You can tell by a careful study of test point F on the cursor board. In the SUBTRACT position, one extra dot should appear before the 512 timing pulses every keypressed. in the ADD position, one short line should eliminate two of the normal 512 timing pulses.
10. An inverter formed from pins 11 and 12 of IC8, cursor board must be placed between IC6 pin 1 and "A" on the cursor board. This is shown correctly on the foil pattern (figure 16) but should be added to the schematic of figure 8.
11. The dot to the left of C14 on figure 8 cursor should be a no connection. Once again, the foil is correct.
12. Connector stack pins 15 and 16 are correct as shown on the foil patterns.  $\emptyset 1$  and  $\emptyset 2$  notation only are apparently backwards in figure 7 and Table III.
13. On the main timing chain schematic, figure six, the LEFT end of C5 should go to R3. The RIGHT end of C6 should go to R2. The foil pattern is correct.
14. In figure 3, mainframe schematic, CURSOR OFF-ON should be S7, not S5.

In general, so far, we have found no errors on the foil patterns. Unless things change with more corrections, always assume the foil pattern and the printed overlay (with the exception of power supply diode D6 overlay) are correct.

Errors are almost inevitable on a project this complicated. My thanks to the readers who have sent in the corrections. Please keep sending them in so we can keep others up to date.